OIP E VOLDOCK

Docket No.: 50032-150

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

Toyohiko KUMAKURA, et al.

Serial No.: 09/237,860

Group Art Unit: 2841

Filed: January 27, 1999

Examiner: K. Cuneo

For: V

WIRING BOARD, SEMICONDUCTOR DEVICE, ELECTRONIC DEVICE,

AND CIRCUIT BOARD FOR ELECTRONIC PARTS

AMENDMENT

Commissioner for Patents Washington, DC 20231

Sir:

The following amendment and remarks are submitted in response to the Office Action dated October 12, 2000.

IN THE SPECIFICATION:

Please replace lines 11 and 12 on page 7 with the following:

b١

FIG. 9 is a sectional view taken along the line 9-9 of FIG. 8;

Please replace the first full paragraph on page 14 with the following rewritten paragraph:

b2

FIG. 7 is a schematic planar view showing an outlined constitution of a DRAM having BGA structure of CSP type according to the second preferred embodiment of the invention. FIG. 8 is an enlarged view showing the essential part of FIG. 7, FIG. 9 is a